

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Bollman

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Applicant: BALAY et al.

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Examiner: DANG, Khanh

Group Art Unit: 2111

Date: November 28, 2006

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U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	SubClass	Filing Date (if appropriate)
AR						
BR						
CR						
DR						
ER						
FR						
GR						
HR						
IR						
JR						
KR						
LR						
MR						
NR						

FOREIGN PATENT DOCUMENTS

		Document Number	Date MM/YYYY	Country	Inventor Name	Class	SubClasses				
								Enclosed	No	Enclosed	No
	OR										
	PR										
	QR										
	RR										
	SR										
	TR										
	UR										
	VR										

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

WR	LATTICE SEMICONDUCTOR CORPORATION, "ORCA@ ORT 4622 Field Programmable System Chip (FPSC) Four Channel x 622 Mbits/s Backplane Transceiver", Preliminary Data Sheet, October 2003pp. 1-79			
XR				
YR				
ZR				
AAR				
BBR				

Examiner

Date Considered:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.